# PSA3600C-LF

## PHASE LOCKED LOOP

Rev A1



#### **FEATURES**

• Frequency Range: 3580 - 3620 MHz

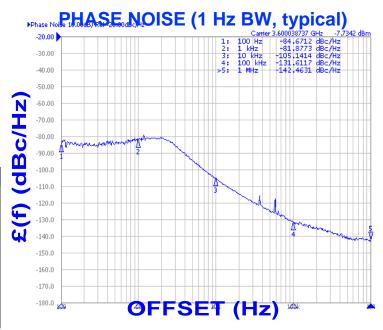
• Step Size: 500 KHz

PLL-24H - Style Package

#### **APPLICATIONS**

- Telecommunications
- Satellite Communication

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PERFORMANCE SPECIFICATIONS	VALUE	UNITS
Frequency Range	3580 - 3620	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-102	dBc/Hz
Harmonic Suppression (2nd, typ.)	-15	dBc
Sideband Spurs (typ.)	-70	dBc
Power Output	0±3	dBm
Load Impedance	50	Ω
Step Size	500	KHz
Charge Pump Output Current	2500	μA
Switching Speed (typ., adjacent channel)	1.5	mSec
Startup Lock Time (typ.)	1.5	mSec
Operating Temperature Range	-40 to 85	°C
Package Style	PLL-24H	
POWER SUPPLY REQUIREMENTS		
Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	31	mA

All specifications are typical unless otherwise noted and subject to change without notice.

#### **APPLICATION NOTES**

• AN-107: How to Solder Z-COMM VCOs / PLLs

• AN-200 : Mounting and Grounding of Z-COMM PLLs

• AN-201 : PLL Fundamentals AN-202 : PLL Functional Description

### **NOTES:**

Reference Oscillator Signal: 5 MHz<f<sub>osc</sub><100 MHz Prescaler: 32

Frequency Synthesizer: Analog Devices - ADF4106

