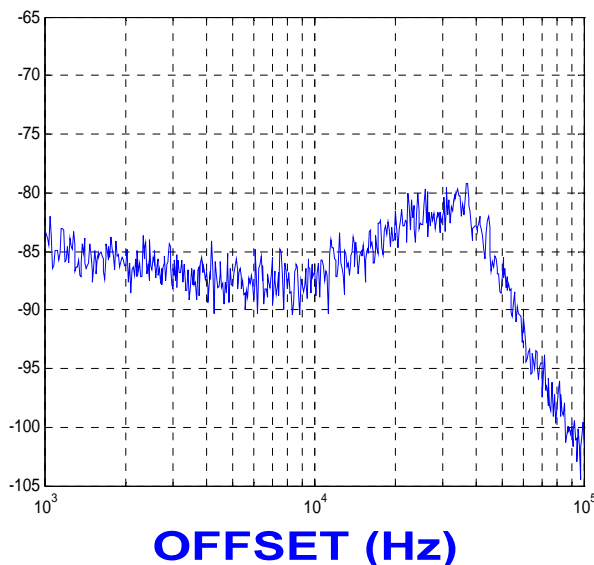


## PHASE NOISE (1 Hz BW, typical)

$\mathcal{L}(f)$  (dBc/Hz)



FEATURES
<ul style="list-style-type: none"> <li>Frequency Range: 3975 - 4025 MHz</li> <li>Step Size: 1000 KHz</li> <li>PLL - Style Package</li> </ul>
APPLICATIONS
<ul style="list-style-type: none"> <li>Microwave Radios</li> <li>Satellite Communications</li> <li>Direct Broadcast</li> </ul>

PERFORMANCE SPECIFICATIONS	VALUE	UNITS
Frequency Range	3975 - 4025	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-85	dBc/Hz
Harmonic Suppression (2nd, typ.)	-15	dBc
Sideband Spurs (typ.)	-65	dBc
Power Output	-0.5±2.5	dBm
Load Impedance	50	$\Omega$
Step Size	1000	KHz
Charge Pump Output Current	5000	$\mu$ A
Switching Speed (typ., adjacent channel)	3	mSec
Startup Lock Time (typ.)	5	mSec
Operating Temperature Range	-40 to 85	$^{\circ}$ C
Package Style	PLL	
POWER SUPPLY REQUIREMENTS		
Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	26	mA

All specifications are typical unless otherwise noted and subject to change without notice.

APPLICATION NOTES
<ul style="list-style-type: none"> <li>AN-107 : How to Solder Z-COMM VCOs / PLLs</li> <li>AN-200 : Mounting and Grounding of Z-COMM PLLs</li> <li>AN-201 : PLL Fundamentals      AN-202 : PLL Functional Description</li> </ul>
<b>NOTES:</b> Reference Oscillator Signal: $5 \text{ MHz} < f_{\text{osc}} < 100 \text{ MHz}$ Frequency Synthesizer: Analog Devices - ADF4113

