PHASE LOCKED LOOP

Rev B2





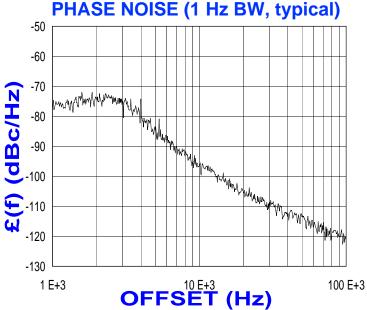
• Frequency Range: 4144 - 4260 MHz

• Step Size: 250 KHz

PLL - Style Package

APPLICATIONS

- Microwave Radios
- Satellite Communications
- Direct Broadcast



PERFORMANCE SPECIFICATIONS	VALUE	UNITS
Frequency Range	4144 - 4260	MHz
Phase Noise @ 10 kHz offset (1 Hz BW, typ.)	-96	dBc/Hz
Harmonic Suppression (2nd, typ.)	-12	dBc
Sideband Spurs (typ.)	-70	dBc
Power Output	0±3	dBm
Load Impedance	50	Ω
Step Size	250	KHz
Charge Pump Output Current	5000	μA
Switching Speed (typ., adjacent channel)	2	mSec
Startup Lock Time (typ.)	2	mSec
Operating Temperature Range	-40 to 85	°C
Package Style	PLL	
POWER SUPPLY REQUIREMENTS		
Supply Voltage (Vcc, nom.)	5	Vdc
Supply Current (Icc, typ.)	26	mA

All specifications are typical unless otherwise noted and subject to change without notice.

APPLICATION NOTES

• AN-107: How to Solder Z-COMM VCOs / PLLs

AN-200 : Mounting and Grounding of Z-COMM PLLs

• AN-201 : PLL Fundamentals AN-202 : PLL Functional Description

NOTES:

Reference Oscillator Signal: 5 MHz<f_{osc}<100 MHz Frequency Synthesizer: Analog Devices - ADF4113

